

Docket No.: 57454-257

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
:
Fukashi MORISHITA :
:
Serial No.: Continuation of :
Appln. Serial No. 09/739,227 : Group Art Unit: Not yet assigned
:
Filed: November 15, 2001 : Examiner: Not yet assigned
:

For: INTERNAL POWER SUPPLY VOLTAGE GENERATION CIRCUIT THAT CAN SUPPRESS
REDUCTION IN INTERNAL POWER SUPPLY VOLTAGE IN NEIGHBORHOOD OF
LOWER LIMIT REGION OF EXTERNAL POWER SUPPLY VOLTAGE

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, DC 20231

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

IN THE SPECIFICATION:

On page 1, after the title, please insert the following paragraph:

--This application is a continuation of U.S. Application Serial No. 09/739,227, filed December 19, 2000, which is a continuation of U.S. Application Serial No. 09/149,079, filed September 8, 1998, now U.S. Patent No. 6,184,744.--

IN THE CLAIMS:

Please cancel claim 1 without prejudice or disclaimer and add new claims 19-22.

--19 A level detection circuitry for detecting a difference between a first voltage and a second

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